

Johns Hopkins on the chip: microsystems and cognitive machines for sustainable, affordable, personalised medicine and healthcare

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Semiconductor technology is contributing to the advancement of biotechnology, medicine and healthcare delivery in ways that it was never envisioned – from chip micro-arrays, to scientific grade CMOS imagers and ion sensing arrays to implantable prosthesis. This exponential growth of sensory microsystems has led to an exponential growth of data. Cognitive machines, i.e. advanced computer architectures and algorithms, are carefully co-designed to extract knowledge from such health data making rational decisions and recommendations for therapies. Nano, micro and macro robotics driven by sophisticated algorithms interface to the human body at different levels and scales, from nano-scale molecules to micron-scale cells to networks and all the way to the scale of organisms. The present era is one where semiconductor technology and the ‘chip’ is the foundation of sustainable and affordable personalised medicine and healthcare delivery.

The state of the art: Since the invention of the integrated circuit – the ‘chip’ in short – in the 1950s, the microelectronics industry has seen a remarkable evolution from the centimetre-scale devices created by Jack Kilby [1] to millimetre-scale integrated circuits fabricated by Robert Noyce to today’s 8nm feature size MOS transistors [2]. During this time, not only have exponential improvements been made in the size and complexity of the devices [3], but the Computer Aided Design software and workstation technologies have advanced at a similar pace, enabling the design of complete truly complex systems on a chip (SOC). The advances in the microelectronics industry have also enabled the proliferation of computational fields for bio-informatics, systems biology [4], imaging and multi-scale multi-domain modelling [5].

Medical imaging has witnessed revolutionary progress on device technologies grounded on clear understanding of human physiology and anatomy. The stunning convergence of semiconductor technology and life science research is transforming the landscape of the pharmaceutical, biotechnology, and healthcare industries, signalling the arrival of personalised and molecular imaging diagnosis and treatment, speeding up the pace of scientific discovery, and changing the practice and delivery of patient care [6].

Thanks again to Moore’s law, with the increased resolution and sampling speed of CMOS (complementary metal-oxide semiconductor) based imagers, a huge amount of 3D/4D data are now collected from spatial and temporal sequences of life science events at all different scales from whole organisms to organs to cellular networks to molecules. Using advanced CMOS-based fluorescence imaging technology today one can measure optically intra-cellular calcium concentration as well as voltage levels with high spatial resolution and 1ms temporal resolution, sufficient to visualise voltage and calcium action potential propagation in myocyte cell tissue [7]. Capitalising on the dramatic advances in the scaling of the MOS transistor, analogue CMOS imagers are being replaced by all digital architectures. Sophisticated digital arrays, read-out integrated circuits and computational sensors are currently being developed to extract and quantify the subtle and intricate information from the huge amount of data so that eventually one can better capture the cell and tissue behaviours in visible and infrared wavelengths [8]. Future systems will allow ultrafast-based time resolved single photon spectroscopy using silicon photomultiplier detectors [9]. The development of innovative bio-engineering devices and tools enables better visualisation and understanding of human anatomy, metabolism, and organ function for diagnostic purposes.

Perhaps an even more stunning example of where semiconductor technology scaling and high level integration has led to phenomenal advances in biotechnology aimed at personalised medicine is the development of ion-based, genome sequencing technology [10]. Sequencing that was once only available in major centres is now routinely found in core biotechnology labs. Just as mainframes gave way to personal computers, there may be a shift towards delocalised sequencing at a hospital or clinic near you. This distributed sequencing capability is creating an unprecedented data deluge. By way of example consider a centralised sequencing installation with 100 first-generation Sanger technology sequencers yielding about 2 Giga-bases per week. Moving to the second-generation technology (e.g. [11] HiSeq 2000) 10 second-generation

machines can yield over 2 Tera-bases per week, a 10000-fold throughput. Assuming raw storage of data and 320 bytes/base the 10 machines will produce approximately 10^{15} bytes (1 Petabyte) every week. In comparison, Eric Schmidt – the CEO of Google – estimated that all the information available in the world wide web is roughly 5 Petabytes. Using ion-based sequencing technology, the cost of sequencing an individual human genome is today down to approximately \$10 000, and there is no reason to believe that this cost cannot be decreased by a factor of 10! Whether through tissue and organ imaging, labs-on-chip or genome sequences, biotechnology and modern medical diagnostics are generating a staggering amount of data that needs to be ‘consumed’, i.e. processed intelligently to extract the relevant information.

Parallel to the advances in CMOS semiconductor technology, in the last decade we have witnessed a revolution in microfluidics for biotechnology and the life sciences. Borrowing ideas from silicon semiconductor manufacturing, soft lithography [12] has emerged as a new way of patterning soft materials. Using soft lithography, it has been possible to design and fabricate silicone ‘chips’ [13], fluidic analogs of integrated circuits that include storage, channels, valves and fluidic decoders made of poly(dimethylsiloxane) (PDMS) a soft organic material widely used in the manufacturing of disposable contact lenses. PDMS has become the mainstay of soft lithography for applications in the life sciences. The low cost (approximately fifty times cheaper than silicon), biocompatibility and its ability to be readily adapted to micro-fabrication techniques are among the numerous reasons PDMS has become immensely popular. A start-up company focused on micro-reactors for high throughput protein crystallisation. PDMS based microfluidic systems, or labs-on-chip as they are often called, are transforming microfluidics into laboratory automation [14]. Alas! These microfluidic ‘integrated circuits’ must be coupled to expensive macro-scale laboratory equipment to achieve the functionality necessary for real world healthcare applications. These labs-on-chip are the equivalent to human-scale bio-analytical laboratories with all the plumbing and glassware but without microscopes, optical detectors, cameras or computers! More recently a new paradigm has emerged for the life sciences and biotechnology microsystem design and fabrication, aimed at instrumenting lab-on-chips. The essence of the approach is the integration and embedding of CMOS electronics into PDMS microfluidics, and the approach was demonstrated through a specific system central to life science research and biotechnologies: the cell incubator [15]. In the latter work, disposable microfluidic structures were fabricated to control and manipulate liquids and gases, and were integrated with reusable functional CMOS silicon blocks. The latter approach is aimed at simple, inexpensive fabrication methods for components with elementary fluidic functions for use in tandem with more expensive, highly functional CMOS components that can be created once and reused many times in the same or other systems.

Fast forward to the future: Going forward to the future the path may be well marked but certainly not without obstacles. Every endeavour that relies on an exponential growth of knowledge and technology and rapidly advancing multi-disciplinary fields is inevitably going to offer challenges, but at the same time opportunities. These challenges often go beyond the traditional scientific and technological issues and into regulatory, financial and policy matters that often transcend single nations and individual societies. The limited available space does not allow us to address patients’ safety, privacy concerns and ethical issues which are of course of paramount importance. In the following Sections we will focus on two such challenges and point to opportunities for future technological advances: (i) computing in the era of exponential data growth; and (ii) the sustainable manufacturing of future bio-electrofluidic chips.

Computing in an era of exponential data growth: Alex Szalay, a physicist and computer scientist in the Physics department at Johns Hopkins University argues that the successive generation of inexpensive sensors is creating an exponential growth of data [16]. While his observation is made based on his experience with astronomy data, this observation holds true for the field of biotechnology. Extracting knowledge from this exponential growth in data generation has become a serious issue. The biggest engineering and technological challenges in advancing personalised medicine are related to the integration, analysis, and interpretation of the massive quantities of heterogeneous biological, chemical, imaging, clinical and cognitive data available as a result of the rapid advances in data collection during the past few decades. Never before has the merger between engineering, computer science

and biomedicine been so strong and there needs to be an ever stronger effort to bridge and increase productivity at the interface between these two disciplines.

However there are dark clouds gathering on the horizon. CMOS computing technology has roughly doubled in performance every 18 months since the first digital computers, a trend popularly known as Moore's law. If we were to extrapolate this trend, we could expect the Exaflop machine (10^{18} Flops = 1 Exaflop) in seven years' time. However, continued miniaturisation of CMOS technology is reaching the physical limit at which further increase in performance will lead to an exponential increase in energy consumption from leakage currents. Fundamental physics (Boltzmann's law) does not allow the physical implementation of a MOS transistor that could behave as a perfect switch, i.e. with zero current flowing when in the off state. Transistors must always conduct tiny amounts of current in their off state. With modern digital integrated circuits having billions of transistors on them, these tiny currents add up to be a significant power dissipation problem. Optimising devices for higher speed has the unfortunate consequence of increasing these leakage currents. What is even more surprising is that none of the advances in non-silicon nano-technologies adequately address the fundamental challenges at the sunset of silicon CMOS technology.

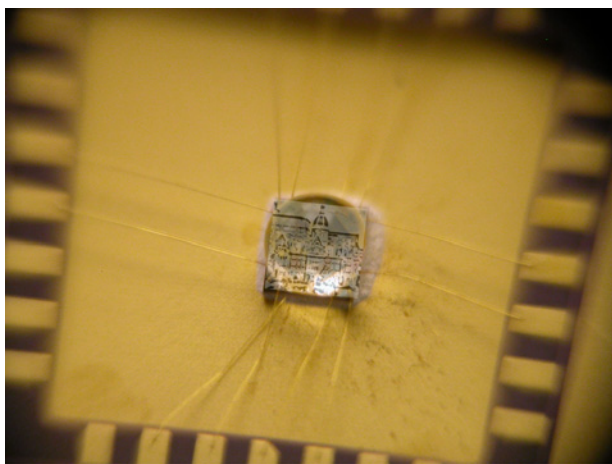


Fig. 1 Magnetic trap chip designed to manipulate micro- and nano-scale functionalised particles and rods

All manipulations performed in drop of water that maintains its shape through the hydrophilic/hydrophobic properties of the metal and dielectric structures that are designed on the surface of the chip. What used to be complicated analysis processes involving samples carried by humans from one room to another can now be accomplished in a single drop of water and chip-scale instrumentation [17]

Clock frequency therefore stagnated in 2005, and today the industry is trending towards massive parallelism, i.e. increasing the number of processors per chip and with the processors operating at slower speeds. The performance of the TOP500 supercomputers, the workhorses in the pharmaceutical industry, has continued to increase by a factor of 1.8 per year on average between 1994 and 2010. However, in 2010 the factor dropped to its all-time minimum of 1.4.

Much like mining and heavy industrial manufacturing, pervasive computing at the Exascale 10^{18} and beyond comes at an environmental cost. Today one Petaflop (10^{15} floating operations per second) requires approximately 10MW of power at a cost of approximately 1 million US dollars a year. Seven years from now an Exaflop machine will necessitate approximately 120MW. In 2007 the collective carbon footprint was comparable to that of the global aviation industry (830 million tons of CO_2) and is expected to increase to 1.4 billion tons of CO_2 by 2020, amounting to 4% of the total carbon emissions. Computing centres alone are responsible for 150 millions tons of CO_2 per year (corresponding to 30 million cars). The projected trend of increased computational performance is not sustainable from the point of view of energy consumption, energy cost, and environmental impact [18].

To meet the scientific demand for Exascale computing, we urgently need new energy efficient or 'green' computing technologies [19]. Today's high-performance computing architectures have not been designed or optimised for data intensive computing. The need for exploring alternative architectures is imminent and minimising energy

consumption is central to further progress, especially in this era of exponential data growth. Advances in computer architecture aimed at data intensive computing exploit heterogeneous storage technologies to achieve high IO throughput [20], as well as systematic methodologies to design chip-multi-processor systems [21].

One of the most exciting developments in technology is the emergence of 3D CMOS, which allows stacking multiple wafers on top of each other thus allowing the placement of main memory in close proximity to the processor. By doing so one can see big savings, a factor of 100 or more in the energy expended to access main memory. This is a big deal! In essence we see computing technology coming closer to the organisation of the brain that has a layered laminar structure where neurons communicate both laterally within the layer and vertically between layers [22]. Inference engines and bioinspired cognitive machines in 3D CMOS technology have the potential of breaking through the exponential data explosion bottleneck. To accelerate progress, however, it is necessary to forge a coordinated effort between computer scientists, engineers, and scientists in specific domain disciplines (e.g. supra-molecular chemistry or neuroscience). The Human Brain project [23] spearheaded by Professor Henry Markram at EPFL is an excellent example of the coordination between academia and industry in a global international setting that provides fertile ground to tackle the grand challenge problem of linking human cognitive behaviour to molecules in the brain, bringing us one step closer to personalised treatment of neurological diseases.

Sustainability in biotechnology growth: In an era where bio-devices will be pervasive and disposable, the hidden environmental and energy costs of their production must be taken into account. It is surprising to many yet true that manufacturing silicon chips outweighs the costs of using them through their lifetime. The environmental impact of the semiconductor industry was first explored in a paper, in which the authors follow the lifetime of a DRAM microchip from its fabrication to its utilisation by consumers by analysing the material and energy that needs to be expended in order to create one chip [24]. The information in this paper came from several resources, including a United Nations Environment Programme, (UNEP) and United Nations Industrial Development Organisation (UNIDO) joint publication and a study published by the Microelectronics and Computer Technology Corporation (MCC). Overall, several forms of chemicals including deposition/dopant gases, etchants, acids and bases, and photolithographic chemicals are needed. It is estimated that 45.2 grams of chemicals are necessary for the production of 1 square centimetre of semiconductor. The largest composition comes from elemental gases of which the largest contributor is nitrogen gas. Aggregate chemical usage information widely differs from source to source, anywhere from 1.2 grams of water per square centimetre of fabricated wafer (national level study conducted by the Toxic Release Inventory) to 610 g/per sq cm (UNEP/UNIDO), with a baseline of 45 g/ per sq cm from the firm. Approximately 18 to 27 litres of water is necessary to manufacture 1 square centimetre of wafer. Energy consumption is separated primarily into two divisions – purifying silicon material into silicon wafers and the processing of the wafer into a chip. The first process that takes quartz (sand) and turns it into a silicon wafer totals nearly 3000 kWh per kilogram of fabricated silicon wafer. It takes about 1.5 kWh to process 1 square centimetre of silicon. On a per-chip basis, including consumer use, in its lifetime, it will use up approximately 56 MJ of energy. Every chip manufactured today must be used for about two years to accumulate energy consumption during operation needed to outweigh the energy cost of manufacturing. Going forward we need a new paradigm for system integration and fabrication of semiconductor-based systems-on-chip for biotechnology, especially those that will have a very short lifetime (use and dispose). We believe: it's time to reinvent the 'chip'! In an era where the semiconductor industry will perhaps be fuelled by applications in healthcare and biotechnology, where devices are 'consumed' very quickly, the 'chip' must be produced in a more efficient, environmentally sound and sustainable manufacturing process. Our vision of a 'chip' begins with organic materials that form the foundation for electronic, photonic and fluidic structures.

From a system perspective, organic semiconductor-based integrated circuit technologies [25] offer attractive solutions where electronics must be spread over a large surface area and can be manufactured with low-cost methods, such as printing [26]. We believe bio-electrofluidics will provide the nexus to large area sensors and actuators, and

photovoltaics for energy harvesting that will further replace traditional silicon technologies in medical and biotechnologies. The question is: how? In all of these applications, a complete, integrated solution is required. Organic integrated structures of centimetre dimensions must be created for containing and transporting fluids, gases and matter. Complementary to these are organic integrated electronic and photonic structures of lower complexity, such as optical waveguides, couplers, photo-batteries for energy harvesting and super-capacitors for energy storage. Much more complex but with smaller dimensions (in the hundreds of microns) are silicon CMOS blocks to provide the necessary ultra-low power digital computations to extract information at the point where inanimate meets the animate world. The essence of the approach goes one step beyond the chip-scale incubator [15] through the inclusion of organic centimetre size active, organic multifunctional substrates. Embedded CMOS super-blocks will continue to provide *in situ* the ultra-low-power information processing that we enjoy today in our entertainment and communication devices. By incorporating parallel processing at the lab-on-chip level, we are assured that our exponential data growth [16] is matched with a distributed parallel growth of computing resources. The components for this 'lab-on-chip-Lego' system will come in parts: a roll of organic multifunctional optofluidic and electrofluidic substrate and a box of CMOS blocks that can be attached to the substrates. Our ultimate goal is the design and synthesis of flexible structures, networks at multiple physical scales in hybrid animate/inanimate technologies that can transduce, adapt, compute, learn and operate under a closed loop for bio-environments. To achieve our goal we must address fundamental questions at the interface of biological and physical systems as we strive to engineer new forms of complex informed matter and three dimensional computational substrates.

Conclusions: As we begin to make small strides in the twenty-first century we are witnessing the birth of an integrative discipline that goes beyond individual domains and is aimed at the automation of the processes and systems that that will enable sophisticated real-time prediction as well as closed loop diagnosis and disease treatment. More importantly, the same technologies will help individual behaviour modification and help encourage a healthier lifestyle.

Today electrical interfaces to the organs of the human body are routinely used to yield information on aggregate behaviour and the state of the system, for example EEG or EKG. Imaging technologies have advanced to the point that soon one will be able to measure the distribution of a specific molecule within the three dimensional structure of a single cell or a patch of myocytes of the human heart while the person is going about their daily exercise routine. However, the subtle yet so important molecular signalling pathways within a cell and in the tissue that have predictive power in the diagnosis of system malfunction can only be interrogated using imaging technologies that are able to sense and measure localised changes in ion concentrations, concentration of a particular chemical and perhaps even key reaction rate constants, as a function of time and space in the tissue. Once this basic capability is achieved one would see a proliferation of implantable devices that would monitor the internal state of the human body, performing diagnosis and therapy such as drug delivery in a timely and effective way. The technologies to accomplish this may not be readily available today but are within our grasp in the decades ahead.

With ever more accelerating technological and scientific progress in the life sciences, 50 years from now, we envision that many of the world's premier medicine and healthcare institutions such as Johns Hopkins will be turned into historical museums. Visitors will be able to get a glimpse of how top tier medical care was taught and research was carried out in the twentieth century. Visitors in Baltimore will be taking a tour of the Inner Harbor, Fells Point and up the hill to Johns Hopkins Hospital museum, much like we today would visit Asklepion on the island of Kos in Greece. For two thousand years, since the ancient times of Hippocratic medicine, patients had to visit the place called a hospital or a clinic where then could get treatment. This two thousand year old practice will end. Healthcare will be delivered at home using patient monitor services and chip-level implantable instrumentation. Wireless, electronically programmed chips will control delivery of drugs in a timely and precise fashion based on algorithms that monitor the individual daily function and behaviour. This will be the age of medicine without borders. The profession of a physician will also see a radical transformation and the last doctor of medicine

(MD) degree – as it is known today – will be awarded in 2043, exactly 150 years from the time the Hopkins School of Medicine opened its doors!

50 years from now, a century would have passed from the invention of the first microchip and Moore's law will be no more. However we will be living in an era where the 'chip' – short for the microchip – in its different forms from imagers to ion sequencers to labs-on-chip and cognitive processor units (CogPUs) will provide the underpinnings and the foundation for affordable, state-of-the-art, global personalised medicine and healthcare delivery.

Acknowledgments: J. Lin is thanked for reading and for helping improve the prose of this Letter.

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17 October 2011

doi: 10.1049/el.2011.3233

One or more of the Figures in this Letter are available in colour online.

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